



ITU G.729A/G.729A+B Speech Coder

SIGNALS+SOFTWARE



Processor

Texas Instruments TMS320C6000 DSP range.

Background

The algorithms implemented are the ITU-T recommendations G.729 Annex A and G.729 Annex A + Annex B fixed rate speech coders for internet and multimedia communications. G.729A is a reduced complexity version of G.729 with similar audio performance in most circumstances

G.729A+B comprises of G.729A with the additional option of Annex B. Annex B uses silence compression or discontinuous transmission to reduce the transmitted bit rate during the silent intervals of speech. Voice Activity Detection (VAD) and Comfort Noise Generation (CNG) algorithms are used to enable the transmission of Silence Descriptor (SID) frames during the periods of silence. This provides the additional advantage of using lower processing loads and DSP bandwidth resource during silence intervals.

Features and Performance

- World leading performance
- 44 channels of G.729A on 300MHz device
- 40 channels of G.729A+B on 300MHz device
- Less than 55Kbytes program memory required for G.729A

G.729A	Program Memory		Data Memory			Interrupt Latency (Cycles)	Typical call Period (ms)	Processing Load (MHz)
	Code (Kbytes)	Tables (Kbytes)	Static Memory		Stack Memory (Kbytes)			
			Heap (Kbytes)	Tables (Kbytes)				
Encoder	42.6	0.05	n * 1.6	6.3	2.5	8400	10	n * 5.6
Decoder	19.2	0.05	n * 0.9	5.2	1.7	5340	10	n * 1.1
Encoder + Decoder	51.6	0.10	n * 2.5	6.6	2.5	8400	10	n * 6.7

Table 1 : DSP Requirements for G.729A

G.729A+B	Program Memory		Data Memory			Interrupt Latency (Cycles)	Typical call Period (ms)	Processing Load (MHz)
	Code (Kbytes)	Tables (Kbytes)	Static Memory		Stack Memory (Kbytes)			
			Heap (Kbytes)	Tables (Kbytes)				
Encoder	57.5	0.05	n * 1.9	6.3	2.6	31400	10	n * 5.8
Decoder	23.1	0.05	n * 1.0	5.2	1.7	9270	10	n * 1.6
Encoder + Decoder	68.8	0.10	n * 2.9	6.6	2.6	31400	10	n * 7.4

Table 2 : DSP Requirements for G.729A+B

Note: Processing loads quote worst-case scenarios and n represents the number of channels. Program memory table values are initialisation values. Kbytes equals 1024 bytes

Technical Notes

The software is written using only fixed-point instructions and is compatible with both the TMS320C6000 fixed-point family and the TMS320C6700 floating-point family. It is supplied in both big-endian and little-endian variants.

The ITU-T G.729A recommendation compresses narrowband speech and audio signals at a sample rate of 8kHz to 8 000 bps, using a linear predictive analysis-by-synthesis coding, Conjugate-Structure Algebraic-Code-Excited Linear Prediction (CS-ACELP). The coder operates on 10ms frames to extract the parameters of the CELP coding model. The decoder uses two filters. One is based on a 10th order Linear Prediction (LP) filter, the other is based on an adaptive-codebook approach. Additional performance enhancement is achieved by use of a Post Filter.

Interface Details

The eXpressDSP™ G.729A and G.729A+B software use the IG729 interface specified by Texas Instruments in the eXpressDSP™ developers' kit.

The software is also available in a non-eXpressDSP version with a basic multi-channel interface. The DSP requirements for these versions are similar to those given in Tables 1 and 2.

Availability

G.729A and G.729A+B are available now for a one-off payment and/or royalties depending on the commercial application.

Software for the TMS320C6000 is available for a full range of vocoders including G.711, G.722, G.723.1, G.726, G.728, G.729, G.729+B, and for other communication algorithms. G.729A and G.729A+B are also available for the TMS320C5000.

SIGNALS+SOFTWARE

SIGNALS+SOFTWARE was founded in 1992 as a developer of high quality Digital Signal Processing application software for the communications industry. Supplying to a whole range of customers, including large blue chip corporations, **SIGNALS+SOFTWARE** has quickly established itself as the world leader in DSP software design and production.

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